

Increasingly, software systems are composed at runtime. Yet, the impact of runtime composition on design quality is unknown. Static analysis, a state-of-the-practice approach, has demonstrated that dependency-caused design hotspots make security vulnerabilities more likely, but it does not detect the effect of dynamic dependencies.

In this work, we are creating tooling to identify dynamic dependencies as well as other information that is not available via static, parsing-based approaches, to both determine and augment the information that is missing in static approaches.

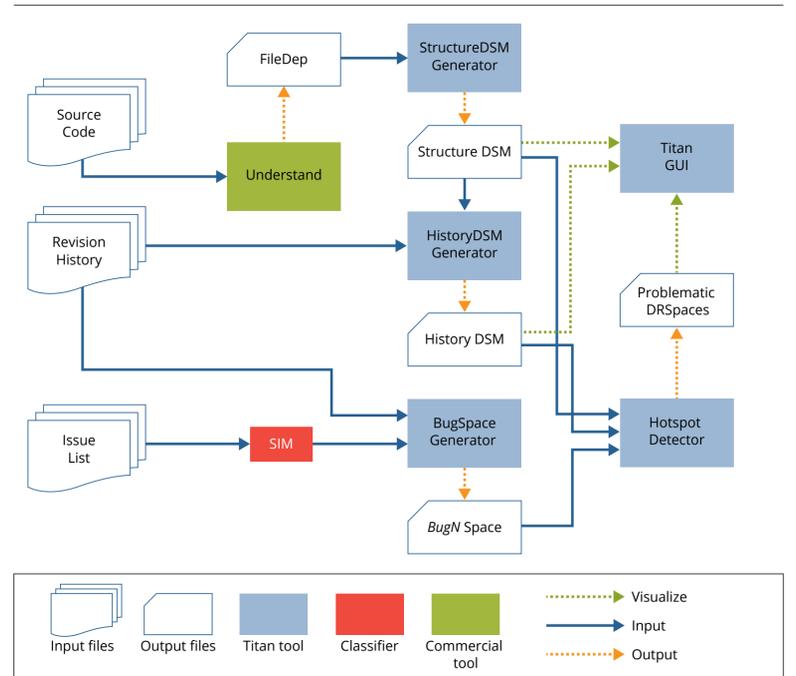
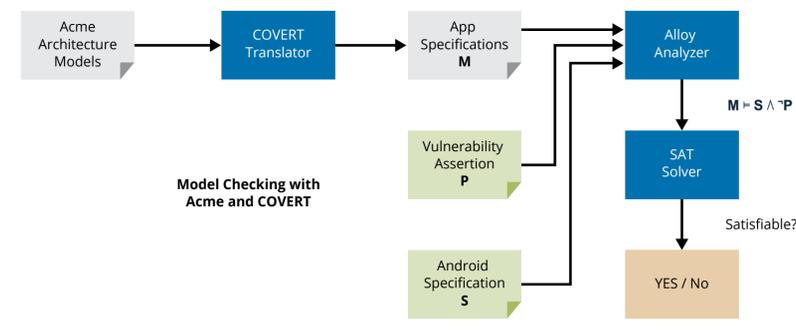
Technical challenges:

1. Detecting dynamic dependencies (DDs)
2. Determining whether DDs create new kinds of architectural flaws
3. Determining the consequences of DD-induced design flaws
4. Proposing refactorings to remedy flaws

Two approaches employed:

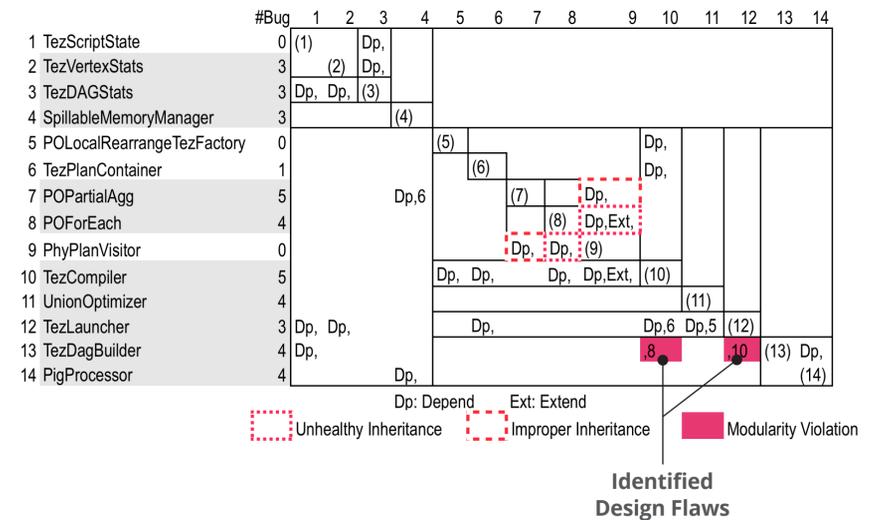
Approach 1: Acme/Alloy-based Analysis

- defined a formal architectural model of the Android framework as an architectural style in Acme
- used Soot to capture an abstract model of each app and translated these models to a single architecture in Acme
- defined Acme invariants to check architecture properties (e.g., apps cannot communicate implicit intents to apps that have a lower trust level)
- translated these models to Alloy specifications
 - checked that privilege escalation is not possible (or provide a counter-example where it is possible)



Approach 2: Titan-based analysis

- reverse-engineering of architecture facts using Understand
- built static representations of architecture dependencies as DSMs
- added historic dependencies from the revision history
- captured potential dynamic relationships as issues from the issue list
- defined a new architecture view: the issue space
- showed that when a system has files revised in many different issues—what we call a hotspot—these “shared” files are connected
- these design dependencies are frequently only seen at runtime and they almost always have design flaws leading to bugs, security flaws, and maintenance problems
- thus they should be analyzed and refactored



Conclusions and Future Work

Design flaws are introduced unknowingly by the daily activities of developers—adding features and fixing bugs. If left untreated they degrade the system over time, making it harder to understand, maintain, extend, and fix.

By considering dynamic information in conjunction with static information we can precisely locate such design flaws, and hence determine the root causes of bugs more quickly.

This information is not available solely via static analysis; dynamic dependencies must be considered. But such information is difficult to obtain. We have explored two methods for doing so.

These design flaws are the roots of technical debt.

In our future work, we are examining the relationship between such design flaws and security bugs.

Copyright 2017 Carnegie Mellon University. All Rights Reserved.

This material is based upon work funded and supported by the Department of Defense under Contract No. FA8702-15-D-0002 with Carnegie Mellon University for the operation of the Software Engineering Institute, a federally funded research and development center.

The view, opinions, and/or findings contained in this material are those of the author(s) and should not be construed as an official Government position, policy, or decision, unless designated by other documentation.

NO WARRANTY. THIS CARNEGIE MELLON UNIVERSITY AND SOFTWARE ENGINEERING INSTITUTE MATERIAL IS FURNISHED ON AN "AS-IS" BASIS. CARNEGIE MELLON UNIVERSITY MAKES NO WARRANTIES OF ANY KIND, EITHER EXPRESSED OR IMPLIED, AS TO ANY MATTER INCLUDING, BUT NOT LIMITED TO, WARRANTY OF FITNESS FOR PURPOSE OR MERCHANTABILITY, EXCLUSIVITY, OR RESULTS OBTAINED FROM USE OF THE MATERIAL. CARNEGIE MELLON UNIVERSITY DOES NOT MAKE ANY WARRANTY OF ANY KIND WITH RESPECT TO FREEDOM FROM PATENT, TRADEMARK, OR COPYRIGHT INFRINGEMENT.

[DISTRIBUTION STATEMENT A] This material has been approved for public release and unlimited distribution. Please see Copyright notice for non-US Government use and distribution.

Internal use:* Permission to reproduce this material and to prepare derivative works from this material for internal use is granted, provided the copyright and "No Warranty" statements are included with all reproductions and derivative works.

External use:* This material may be reproduced in its entirety, without modification, and freely distributed in written or electronic form without requesting formal permission. Permission is required for any other external and/or commercial use. Requests for permission should be directed to the Software Engineering Institute at permission@sei.cmu.edu.

* These restrictions do not apply to U.S. government entities.

Carnegie Mellon® is registered in the U.S. Patent and Trademark Office by Carnegie Mellon University.

DM17-0737
Dynamic Design Analysis